

COMPLETE LISTING OF THE CLAIMS

The following lists all of the claims that are or were in the above-identified patent application. The status identifiers respectively provided in parentheses following the claim numbers indicate the current statuses of the claims.

1-13 (Canceled)

14. (Previously Presented) A heterojunction bipolar transistor, comprising:

a sub-collector;

a collector atop the sub-collector;

a base atop the collector;

a base contact atop the base;

an emitter atop the base, the emitter comprising a first sidewall with a first reentry feature consisting entirely of a first undercut profile;

an emitter cap atop the emitter, the emitter cap comprising a second sidewall with a second reentry feature consisting entirely of a second undercut profile; and

an emitter metal atop the emitter cap.

15. (Canceled)

16. (Previously Presented) The transistor of claim 14, wherein the collector comprises a third sidewall with a third reentry feature consisting entirely of a third undercut profile.

17. (Previously Presented) The transistor of claim 16, wherein the base comprises a fourth sidewall with a fourth reentry feature consisting entirely of a fourth undercut profile.